

3GIO ----- 3rd Generation I/O
AACS ----- Advanced Access Content System
ADC ----- Analog Digital Converter
AGP ----- Accelerated Graphics Port
API ----- Application Programmin Interface
APS ----- Analog Protection System
ASPI ----- Advanced SCSI Programming Interface
ATA ----- Advanced Technology Attachment
ATAPI ----- ATA Packet Interface
BIOS ----- Basic Input Output System
CAM ----- Common Access Method
CAV ----- Constant Angular Velocity
CCS ----- Common Command Set
CD ----- Compact Disk
CGA ----- Color Graphics Adapater
CHS ----- Cilinder Head Sector
CISC ----- Complete Instruction Set Computer
CLV ----- Constant Lineair Velocity
CRC ----- Cyclic Reduncy Check
CSS ----- Content Scramble System
CUDA ----- Compute Unified Device Architecture
DAC ----- Digital Analog Converter
DAM ----- Data Address Marking
DAO ----- Disk At Once
DDRDRAM ---- Double Data Rate DRAM
DMA ----- Direct Memory Access
DRAM ----- Dynamic RAM
DVD ----- Digital Versatile Disk
DVS ----- Dynamic Voltage Scaling
EDD ----- Enhanced Disk Drive
EDODRAM ---- Extended Data Output DRAM
EEPROM ----- Electronically EPROM
EFI ----- Extensible Firmware Interface
EFM ----- Eight to Fourteen Modulation
EGA ----- Enhanced Graphics Adapter
EISA ----- Extended ISA
EPROM ----- Erasable Programmable ROM
FAT ----- File Allocation Table
FPMDRAM ---- Fast Page Mode DRAM
GDDR3SDRAM - Graphical DDR3 SDRAM
GPR ----- General Purpose Register
GPT ----- GUID Partition Table
GPU ----- Graphics Processing Unit
HGC ----- Hercules Graphics Card
IAM ----- Index Address Marking
ICH ----- I/O Controller Hub
IDAM ----- ID Address Marking
IDE ----- Integrated Drive Electronics
IEEE ----- Institute of Electric and Electronical Engineers
ILP ----- Instruction Level Parallellism
IRQ ----- Interrup ReQuest
ISA ----- Industry Standard Architecture
JBOD ----- Just a Bunch Of Disks
LBA ----- Logica Block Addressing
LVM ----- Logical Volume Management
MBR ----- Master Boot Record
MCA ----- Micro Channel Architecture
MCH ----- Memory Controller Hub
MDA ----- Monochrome Display Adapter
MDRAM ----- Multibank DRAM
MFT ----- Master File Table
MMX ----- MultiMedia eXtensions
MSDOS ----- MicroSoft Disk Operating System
MTBF ----- Mean Time Between Failures
MTTF ----- Mean Time To Failure
MTTR ----- Mean Time To Repair
NMI ----- Non-Maskable Interrupt

NRZI ----- Non-Return to Zero Inverted
NTFS ----- New Technology File System
NVRAM ----- Non-Volatile RAM
OEM ----- Original Equipment Manufacturer
PCI ----- Peripheral Component Interconnect
PCIe ----- PCI express
PIC ----- Programmable Interrupt Controller
PNP ----- Plug aNd Play
POST ----- Power On Self Test
PRML ----- Partial Response Maximum Likelihood
QAS ----- Quick Arbitrate and Select
RAID ----- Redundant Array of Independant Disks
RAM ----- Random Access Memory (Read And Modify)
RAMDAC ----- RAM Digital Analog Converter
RISC ----- Reduced Instruction Set Computer
ROM ----- Read Only Memory
RPC ----- Region Playback Control
SAS ----- Serial Attached SCSI
SCAM ----- SCSI Configured AutoMagically
SCSI ----- Small Computer System Interface
SDRAM ----- Synchronous DRAM
SGRAM ----- Synchronous Graphics DRAM
SIMD ----- Single Instruction Multiple Data
SMART ----- Self Monitoring Analysis and Reporting Technology
SMP ----- SCSI Management Protocol
SMPS ----- Switche Mode Power Supply
SPEC ----- System Performance Evaluation Corporation
SPI ----- SCSI Parallel Interface
SRAM ----- Static RAM
SSE ----- Streaming SIMD Extensions
SSP ----- Serial SCSI Protocol
STP ----- SCSI Tunneled Protocol
SVGA ----- Super VGA
TAO ----- Track At Once
TLP ----- Thread Level Parallellism
UDF ----- Universal Disk Format
USB ----- Universal Serial Bus
USN ----- Update Sequence Number
UVGA ----- Ultra VGA
VESA ----- Video Electronics Standard Architecture
VFAT ----- Virtual FAT
VGA ----- Video Graphics Array
VRAM ----- Video RAM
WRAM ----- Window RAM
XGA ----- eXtended Graphics Array
kWIPS ----- kilo Whetstone Instructions Per Second